

Refine Search

Search Results -

Term	Documents
INDICES	98596
INDEX	1552298
INDEXES	57295
INDEX\$3	0
INDEX	1552298
INDEXA	74
INDEXAB	9
INDEXABE	1
INDEXABI	1
INDEXABL	39
INDEXACT	2
(L21 AND (INDEX\$3 OR INDICES)).PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD.	154

There are more results than shown above. Click here to view the entire set.

Database:

US Pre-Grant Publication Full-Text Database
 US Patents Full-Text Database
 US OCR Full-Text Database
 EPO Abstracts Database
 JPO Abstracts Database
 Derwent World Patents Index
 IBM Technical Disclosure Bulletins

Search:

L24

Refine Search

Recall Text



Clear

Interrupt

Search History

DATE: Thursday, October 27, 2005 [Printable Copy](#) [Create Case](#)

Set Name **Query**
 side by side

Hit Count **Set Name**
 result set

DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR

<u>L24</u>	L21 and (index\$3 or indices)	154	<u>L24</u>
<u>L23</u>	L21 and (index\$3 or indices or point\$3 or number\$3 or position\$1)	234	<u>L23</u>
<u>L22</u>	L21 and (index\$3 or indiuces or point\$3 or number\$3 or position\$1)	234	<u>L22</u>
<u>L21</u>	l19 and (valid\$7 or invalid\$7)	234	<u>L21</u>
<u>L20</u>	L18 and l9	68	<u>L20</u>
<u>L19</u>	L18 and l7	279	<u>L19</u>
<u>L18</u>	l3 and scoreboard	451	<u>L18</u>
<u>L17</u>	L15 and l9	11	<u>L17</u>
<u>L16</u>	L15 and l7	25	<u>L16</u>
<u>L15</u>	l5 and (instruction\$1 or microinstruction\$1 or command\$3)	45	<u>L15</u>

DB=PGPB,USPT; PLUR=YES; OP=OR

<u>L14</u>	l6 and l9	11	<u>L14</u>
<u>L13</u>	l6 and l7	25	<u>L13</u>
<u>L12</u>	l4 and l9	12	<u>L12</u>
<u>L11</u>	l4 and l7	29	<u>L11</u>
<u>L10</u>	l3 and L9	435	<u>L10</u>
<u>L9</u>	(712/26,217)![CCLS]	569	<u>L9</u>
<u>L8</u>	(712/2-300)[CCLS]	11509	<u>L8</u>
<u>L7</u>	(712/2-300)![CCLS]	11509	<u>L7</u>

DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR

<u>L6</u>	L5 and l3	35	<u>L6</u>
<u>L5</u>	L4 and (valid\$7 or invalid\$7)	47	<u>L5</u>
<u>L4</u>	scoreboard near8 entr\$4 near15 (index\$3 or indices or point\$3 or number\$3 or position\$1)	57	<u>L4</u>
<u>L3</u>	(associat\$5 or assign\$6 or map\$6) near6 (instruction\$1 or microinstruction\$1 or command\$3)	75655	<u>L3</u>
<u>L2</u>	L1 and (valid\$7 or invalid\$7)	47	<u>L2</u>
<u>L1</u>	scoreboard near8 entr\$4 near15 (index\$3 or indiuces or point\$3 or number\$3 or position\$1)	57	<u>L1</u>

END OF SEARCH HISTORY


[Home](#) | [Login](#) | [Logout](#) | [Access Information](#) | [Alt](#)

Welcome United States Patent and Trademark Office

Search Results

[BROWSE](#)[SEARCH](#)[IEEE XPLORE GUIDE](#)

Results for "(((scoreboard)<in>metadata))<and>((scoreboard)<in>metadata) and entr)"

☒ e-mail

Your search matched 5 of 15 documents.

A maximum of 100 results are displayed, 25 to a page, sorted by **Relevance in Descending order**.

» Search Options

[View Session History](#)[New Search](#)

Modify Search

(((scoreboard)<in>metadata))<and>((scoreboard)<in>metadata) and entr)"

☐ Check to search only within this results set

» Key

Display Format:



Citation



Citation & Abstract

IEEE JNL IEEE Journal or Magazine

IEEE JNL IEE Journal or Magazine

IEEE CNF IEEE Conference Proceeding

IEEE CNF IEE Conference Proceeding

IEEE STD IEEE Standard

Select Article Information:

**1. An implementation of scoreboarding mechanism for ARM-based SMT processor**

Chang-Yong Heo; Kyu-Baik Choi; In-Pyo Hong; Yong-Surk Lee;

ASIC, 2003. Proceedings. 5th International Conference on

Volume 1, 21-24 Oct. 2003 Page(s):443 - 446 Vol.1

Digital Object Identifier 10.1109/ICASIC.2003.1277581

[AbstractPlus](#) | Full Text: [PDF](#)(343 KB) IEEE CNF**2. Verification of an industrial CC-NUMA server**

Mukherjee, R.; Nakayama, Y.; Mima, T.;

Design Automation Conference, 2002. Proceedings of ASP-DAC 2002. 7th Asia and South Pacific

International Conference on VLSI Design. Proceedings.

7-11 Jan. 2002 Page(s):747 - 752

Digital Object Identifier 10.1109/ASPDAC.2002.995023

[AbstractPlus](#) | Full Text: [PDF](#)(274 KB) IEEE CNF**3. Issue logic for a 600-MHz out-of-order execution microprocessor**

Farrell, J.A.; Fischer, T.C.;

Solid-State Circuits, IEEE Journal of

Volume 33, Issue 5, May 1998 Page(s):707 - 712

Digital Object Identifier 10.1109/4.668985

[AbstractPlus](#) | [References](#) | Full Text: [PDF](#)(120 KB) IEEE JNL**4. The iCore 520-MHz synthesizable CPU core**

Richardson, N.; Lun Bin Huang; Hossain, R.; Lewis, J.; Zounes, T.; Soni, N.;

Micro, IEEE

Volume 23, Issue 3, May-June 2003 Page(s):46 - 57

Digital Object Identifier 10.1109/MM.2003.1209466

[AbstractPlus](#) | [References](#) | Full Text: [PDF](#)(450 KB) IEEE JNL**5. Formal design verification for correctness of pipelined microprocessors with out-of-order in**

Takenaka, T.; Kitamichi, J.; Higashino, T.; Taniguchi, K.;

Design Automation Conference, 1999. Proceedings of the ASP-DAC '99. Asia and South Pacific

18-21 Jan. 1999 Page(s):177 - 180 vol.1

Digital Object Identifier 10.1109/ASPDAC.1999.759989

[AbstractPlus](#) | Full Text: [PDF](#)(376 KB) IEEE CNF